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Bib Data Sheet

CONFIRMATION NO. 9219

|                             |                                       |              |                        |                                   |
|-----------------------------|---------------------------------------|--------------|------------------------|-----------------------------------|
| SERIAL NUMBER<br>10/799,742 | FILING DATE<br>03/12/2004<br><br>RULE | CLASS<br>365 | GROUP ART UNIT<br>2827 | ATTORNEY<br>DOCKET NO.<br>CD03011 |
|-----------------------------|---------------------------------------|--------------|------------------------|-----------------------------------|

APPLICANTS *-ck*

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\*\* CONTINUING DATA \*\*\*\*\* *ck*

This appln claims benefit of 60/458,036 03/27/2003

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *ck*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/28/2004

|  |  |                           |                        |                                     |                            |
|--|--|---------------------------|------------------------|-------------------------------------|----------------------------|
| Foreign Priority claimed<br>35 USC 119 (a-d) conditions met<br>Verified and Acknowledged | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no<br><input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance<br>Examiner's Signature <i>sm</i> Initials | STATE OR<br>COUNTRY<br>VT | SHEETS<br>DRAWING<br>9 | TOTAL<br>CLAIMS<br><del>10</del> 15 | INDEPENDENT<br>CLAIMS<br>2 |
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## TITLE

Method and circuit for reducing defect current from array element failures in random access memories

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